

Abstract of the Disclosure:

The power switch has a first transistor, a limiting transistor, and an auxiliary transistor. The first transistor has a load path and a control electrode. The limiting transistor, which limits a voltage drop across the load path of the first transistor, has a load path connected in series with the load path of the first transistor, and a control electrode. The auxiliary transistor has a load path connected between the control electrode of the limiting transistor and a reference node, and having a control electrode connected between the first transistor and the limiting transistor.

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